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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/511,837	10/19/2004	James Albert Wilber	PU020136	6557

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EXAMINER	
A, MINH D	

ART UNIT	PAPER NUMBER
2821	

DATE MAILED: 03/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/511,837	WILBER ET AL.	
	Examiner	Art Unit	
	Minh D. A	2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *19 October 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-12* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-5,7 and 9-12* is/are rejected.

7)☒ Claim(s) *6 and 8* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *10/19/04*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

1. This is a response to the Applicant's filing on10/19/04. In virtue of this filing, claims 1-12 are currently presented in the instant applicant.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Inventorship

3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-5, 9 and 11-12 are rejected under 35 U.S.C. 102(b) as being anticipated by Wilensky et al (US 4,472,707).

Regarding claim 1, Wilensky discloses a display processor digital and enhanced resolution comprising: a first digital-to-analog converter (11) responsive to a digitally encoded signal containing magnetic field compensation information; for generating a first analog signal containing the magnetic field compensation information(deflection coil) from said digitally encoded signal; a magnetic field compensation winding positioned on a cathode ray tube; an amplifier (13) responsive to said first analog signal and having an output that is coupled to said magnetic field compensation winding(X deflection coil) for producing a current in said magnetic field compensation winding, said current producing a magnetic field in a beam of said cathode ray tube that compensates for an ambient magnetic field; and a second digital-to-analog converter (15) for generating a second analog signal that is coupled to an input of said amplifier(15) that varies said current in accordance with said second analog signal. See figure 1, col.3, lines 44-67 to col.7, lines 1-47.

Regarding claim 2, Wilensky discloses wherein said second analog signal tracks variations in said first analog signal for providing error compensation and wherein said magnetic field compensation current is controlled processor (23)) in accordance with a difference between said first and second analog signals. See figure 1.

Regarding claim 3, Wilensky discloses the digital-to-analog converters (13 and 15) are separate units contained in a single integrated circuit. See figure 1.

Regarding claim 4, Wilensky discloses the digital-to-analog converters(13 and 15) are energized from a common supply voltage. See figure 1.

Regarding claim 5, Wilensky discloses 5. The apparatus according to Claim 1, wherein said amplifier comprises a differential, input stage, wherein said first analog signal is coupled to one of an inverting input and a non-inverting input of the amplifier (13)and wherein said second analog signal is coupled to the other one of said inverting and non-inverting input of said amplifier. See figure 1.

Regarding claim 9, Wilensky discloses a magnetic field compensation winding positioned on a cathode ray tube to produce a magnetic field in a beam path of said cathode ray tube to compensate for an ambient magnetic field; an amplifier having an output that is coupled to said winding for generating a magnetic field compensation current in said winding; and a first semiconductor stage and a separate, second semiconductor stage for generating a first signal and a second signal, respectively, that are coupled to said amplifier to control said magnetic field compensation current, in accordance with a difference between said first and second signals, such that a deviation from an initial value selected for said first signal is compensated by a corresponding change in a value of said second signal. See figure 1, col.3, lines 44-67 to col.7, lines 1-47.

Regarding claim 11, Wilensky discloses a first semiconductor stage(11) for generating a first signal containing magnetic field compensation information; a magnetic field compensation wining positioned on a cathode ray tube; an amplifier(13) responsive to said first signal and having an output that is coupled to said magnetic field

compensation winding(X deflection coil) for producing a current in said magnetic field compensation winding, said current producing a magnetic field in a beam path of said cathode ray tube that compensates for an ambient magnetic field; and a second semiconductor stage(12) in a common integrated circuit for generating a second signal that is coupled to an input of said amplifier to compensate for an error introduced in said first signal. See figure 1, col.3, lines 44-67 to col.7, lines 1-47.

Regarding claim 12, Wilensky discloses each of said semiconductor stages comprises a corresponding digital-to-analog converter. See figure 1.

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

7. Claims 7 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over by Wilensky et al (US 4,472,707).

Regarding claims 7 and 10, Wilensky essentially discloses the claimed invention but does not explicitly disclose that the usages current negative feedback path coupled to said amplifier to reduce a dependency of said current on an impedance of the winding or the semiconductor stages are contained in a single integrated circuit.

It would have been an obvious matter of design choice to employ Wilensky in any desired interest area in order to maximize the usage of his invention, since applicant does not disclose that, all of these limitations can solve any stated problem and for any particular purpose. Therefore, it appears that the invention would not provide any improvement but merely apply the invention in different presentation.

Allowable Subject Matter

8. Claims 6 and 8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Prior art does not teach that, the compensation winding is coupled to a source of a supply voltage, wherein a polarity of the current varies in accordance with a difference between an output voltage of said amplifier and said supply voltage and wherein a polarity of said amplifier output voltage remains the same both when said current is at a first polarity and when said current is at a polarity that is opposite to said first polarity recited in dependent claim 6.

Prior art does not teach that, a source of a second digitally encoded signal coupled to an input of said second digital-to-analog converter, wherein during a degaussing interval, said value of said first and second digitally encoded signals are made to be equal for preventing the generation of said current recited in dependent claim 8.

Citation of relevant prior art

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Prior art Lee et al. (U.S. Patent No. 5,847,511) discloses an automatic image rotation compensation circuit and method.

Prior art Lee (U.S. Patent No. 6,013,989) discloses a wideband horizontal linearity correction circuit.

Prior art Johnston. (U.S. Patent No. 4,344,021) discloses a system for compensating for transfer characteristic variations of electron guns.

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh Dieu A whose telephone number is (571) 272-1817. The examiner can normally be reached on M-F (5:30 AM-2:45 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Callahan Timothy can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Shih-Chao Chen

SHIH-CHAO CHEN
PRIMARY EXAMINER

Examiner

Minh A

Art Unit 2821

3/13/06